

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

301/49887

Application Number

09/581,464

Applicant(s)

M. Seifert, et al.

Filing Date

6/9/00

Group Art Unit

2667

Not Yet Assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
au	BA	GB 2 319 128 A	5/13/98	United Kingdom	—	—	✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	CA	R. Senthinathan et al., IEEE Journal of Solid-State Circuits 28 (1993) December, No. 12, p. 1383-1388

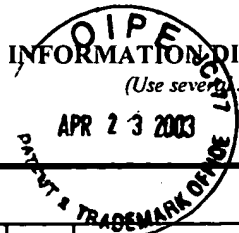
EXAMINER

Anh-Vu Ly

DATE CONSIDERED

10/17/03

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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Docket Number (Optional)
49887 (70364)

Application Number
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Applicant(s)
SEIFERT, et al.

Filing Date
06.09.00

Group Art Unit
2731 2667

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

RECEIVED

APR 25 2003

Technology Center 2600

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
aw	BB	1-236731	09.21.89	JAPAN	—	—	Partial Translation	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER Anh-Vu Ly	DATE CONSIDERED 10/17/03
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